



2024 Attendee List At-A-Glance

| Job Title | Company |
|--|--|
| Director | 2nd Sources |
| Engineer | 3G Shielding Specialties |
| Principle Product Development Specialist | 3M |
| CEO & President | ABC Assembly, Inc. |
| Global Director | AB-InBev |
| Principal Engineer | Accelink USA Corp |
| CEO | Accentec Technologies LLC |
| Senior Director | Aces Electronics |
| FAE Manager | ACON |
| System Engineer | Acroview Technology Corp., |
| VP Engineering | Adapt-IP |
| Senior Application Engineer | ADI |
| VLSI Design Engineer | ADSANTEC |
| RF Engineer | ADTEC |
| CEO | Advanced Interconnect Technologies |
| Silicon Design Engineer | Advanced Micro Devices |
| Senior Engineer | Advantage Electronic Design Services LLC |
| Principal Engineer | Advantest America Inc |
| Application Engineer | Advantest America Inc |
| Senior Engineering Scientist | AEi Systems |
| Failure Analysis Engineer | Aeva Inc. |
| Director of Silicon Programs | Aeva, Inc |
| President | Aledyne Engineering |
| Sr. Software Engineer | Aledyne Engineering |
| Engineering Director | Alibaba |
| Software Engineer | Align |
| DVT Engineer | Allied Telesis |
| Director | Allion Test Labs, Inc. |
| COO | Alltracpcb LLC |
| Principal SI & PI Engineer | Alphawave Semi |
| Application Engineer - Packaging | Alphawave Semi |
| Solution Engineer | Alphawave Semi |
| Design Engineer | Alphawave Semi |
| Director, Software Development | Altair Engineering, Inc. |
| Co-founder and Managing Director | Altasights |
| Engineering Manager HW Development | Amazon |
| Senior Product Manager | Amazon |
| Sr. ESD System Design Engineer | Amazon |
| Sr. FPGA Design Engineer | Amazon |
| RF/EMC Engineer | Amazon |
| Principal Hardware Systems Engineer | Amazon |

| Job Title | Company |
|--|--------------------------|
| Memory Validation Engineer | Amazon |
| Software Development Engineer II | Amazon Web Services Inc. |
| PMTS Analog Design Engineer | AMD |
| SMTS Silicon Design Engineer | AMD |
| PMTS Silicon Design Engineer | AMD |
| Design Engineer | AMD |
| Socket Development Engineer | AMD |
| SMTS Silicon Design Engineer | AMD |
| Distinguished Engineer | Ampere Computing |
| SOC Test Engineer | Ampere Computing |
| Validation Engineer | Ampere Computing |
| Hardware Validation Engineer | Ampere Computing |
| Product Manager, Modular Jacks – ACPA | Amphenol |
| Chief Technology Officer - Commercial Products | Amphenol |
| Director of Engineering Cable Backplane Systems | Amphenol |
| Chief Technology Officer ACS High Speed Products | Amphenol |
| Solutions Architect | Amphenol |
| Cable Backplane SI Engineering Manager | Amphenol |
| Principal Engineer | Analog Devices |
| Principal Applications Engineer | Anritsu Company |
| Principal Application Engineer | Ansys |
| Senior Director Product Management | Ansys |
| System Design Engineer (EMC) | Apple |
| PCB Technologist | Apple |
| Hardware Engineer | Apple |
| Senior Manager | Apple |
| Design Engineer | Apple |
| Senior Principal Engineer | Apple |
| EMC Design Engineer | Apple |
| Systems Design Engineer | Apple |
| Silicon Validation Manager | Apple |
| Systems Engineer | Applied Materials |
| Component Engineer | Arista Networks |
| Optical Test Engineer | Arista Networks |
| Electrical Engineer | Arista Networks |
| Design Engineer | Arista Networks |
| Senior Design and Signal Integrity Engineer | Arista Networks |
| Software Engineer | Arista Networks |
| Systems Engineer | ASA Computers Inc |

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#1 Attendee Priority: See new products/innovations in-person

| Job Title | Company |
|--|-------------------------------------|
| Hardware Design Engineer | Aselsan |
| Systems Design Engineer | Aselsan |
| Hardware Design Engineer | Aselsan |
| Distinguished Engineer, SI/PI | Astera Labs |
| Product Manager | Astera Labs |
| Post Silicon Validation Engineer | Astera Labs |
| Principal Electrical Validation Engineer | Astera Labs |
| Semiconductor Packaging Engineer | Astera Labs |
| Sr. Vice President | Astera Labs |
| Sr. Director | Astera Labs |
| VP | Astera Labs |
| Digital Verification Engineer | Astera Labs |
| Digital Design Engineer | Astera Labs |
| Design Verification Engineer | Astera Labs |
| Principle Design Verification Engineer | Astera Labs |
| Electrical Engineering Team Lead | Astranis |
| Avionics Team Lead | Astranis Space Technologies |
| Edge AI Solution Architect | ASUS/AEON |
| Executive Director | AtmoSense, Inc. |
| Field Applications Engineer (FAE) | Avnet |
| Head of Solutions for Hitech and Semiconductor | AWS |
| Sr Mechanical Engineer | AWS |
| Network Development Engineer | AWS |
| Specialist Engineer | Axis |
| Experienced Hardware Engineer | Axis Communications |
| Director of Hardware Engineering | Bekonix |
| Director of AI and Cloud | Belden |
| Design Engineer | Bellwether |
| Engineering | Bema Electronics |
| CEO | BenchIQ |
| Test Engineer | Bently Nevada |
| Senior Engineer | Bertrand Consulting |
| VP Business Development | Bestartech |
| Senior Software Engineer | Bio Engine Consulting |
| Electrical Engineer | BIOMERIEUX |
| Electrical Engineer V | Biomerieux |
| IoT and Project Manager Consultant | BiPOM Electronics |
| Principal Engineer | BitByte Lab |
| Sr. Project Engineer | BizLink |
| Product Engineer Senior | BizLink Special Cables Germany GmbH |
| Investment Principal | Bosch Ventures |
| Design Consultant | Brace Computer Laboratory |
| Senior Principal Engineer | Cadence Design Systems |
| Design Engineering Director | Cadence Design Systems |
| Applications Engineer | Cadence Design Systems |
| Design Engineering Director | Cadence Design Systems |

| Job Title | Company |
|---|---|
| Senior Systems Engineer | Cadence Design Systems |
| AE Manager | Cadence Design Systems |
| Director Strategic Alliances | Cadence Design Systems |
| Hardware Engineer | Celestica |
| Global Commodity Manager | Celestica |
| Hardware Electro-Mechanical Engineer | Ciena |
| VP Engineering | CIGTECH USA |
| Leader, HW Engineering | Cisco |
| Hardware Technical Leader | Cisco |
| Hardware Engineer | Cisco |
| Signal and Power Integrity Engineer | Cisco |
| President | Clark International Inc. |
| CFO | Clark International Inc. |
| President | Clear Signal Solutions |
| Senior Test Engineer | CNEX Labs |
| Electrical Test Engineer | CODAR |
| Director of Production | Codar Ocean Sensors |
| RF Engineer | Codar Ocean Sensors |
| Test Production Engineer | Codar Ocean Sensors |
| Sr. Director of Engineering | Coherent |
| Engineer | Coherent |
| Sr Director, Test Development | Coherent |
| Director of Product Management | Coherent Inc |
| Architect | Coherent Logix |
| System Analyst | Cole Fox |
| Product Developer | Collective UX Design |
| Senior Engineer | CollTech North America |
| Principal Engineer | Common Ground Electronics |
| Distinguished Hardware Engineer | Commscope |
| Principal Antenna Design Engineer | CommScope Ruckus Networks |
| Engineering Designer | Computer Creations [CCR] |
| Managing Director | Concours Ventures |
| Tech and Innovation Officer | Consulate General of Georgia |
| Consul General | Consulate General of Georgia in San Francisco |
| VP of Platform Engineering | Cornelis Networks |
| Signal Integrity Engineering Manager | Cornelis Networks |
| Validation Engineer | Coros |
| Principle Engineer | Credo Semiconductor |
| Test Engineer | Credo Semiconductor |
| Sr Program Manager | Credo Semiconductor |
| Associate VP of Application Engineering | Credo Semiconductor |
| System Application Manager | Credo Semiconductor |
| Sr. Manager in EE | Cruise |
| Hardware Engineer | Cruise |
| SI Engineer | Cruise |
| Hardware Architecture Manager | D. E. Shaw Research |

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| Job Title | Company |
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| System Architect | Dell Networking |
| Distinguished Engineer | Dell Technologies |
| Solution Engineer | Dell Technologies |
| Principle Systems Engineer | Delta Electronics |
| Design Engineer | Delta Electronics |
| Product Manager | Delta Electronics |
| COO | Denkai America |
| CTO | Department C Inc |
| Global Tech Impact Capitalist | Design Innovation, Stanford |
| Founder | DevHeads IoT Integration Server |
| Chief Hardware/Software Developer | Digital People, Inc. |
| Interface Product Application Director | Diodes Incorporated |
| Sr. Staff Engineer | Dolby Laboratories |
| Staff Engineer | Dolby Laboratories |
| Director, HW Engineering | Dolby Laboratories |
| Hardware Design Engineer | Dolby Laboratories |
| Director | Dolphin Design |
| Principal Engineer | DuPont |
| Senior Manager | DuPont |
| Staff Scientist | Dupont |
| Application Engineer | DuPont |
| EIF Segment Leader | DuPont |
| Technical Program Manager | ECT |
| Product Manager | ECT |
| Data Analyst | Edscapade Games |
| Sr. Acct Director | EE Times & EDN Global |
| Sr. PCB Designer | Empower Semiconductor Inc. |
| Application Engineer | Emprog, Inc. |
| Manager | Emprog, Inc. |
| Test Engineering Consultant | EMS Technologies |
| Head of HW Engineering | Encharge Ai |
| Director | Encore Semi Inc |
| Sales Manager | Endeavor Business Media |
| Sr. PCB Designer | Energous |
| ISO 9001 Consultant | Engineering Network |
| VP of Eng | ENOSEMI |
| Vice President | Enplas Tech Solutions, Inc. |
| Program Manager | Epoch Technologies |
| Principal Network Engineer | Equinix |
| Data Scientist | Ericsson |
| Director of Systems Engineering | Esperanto Technologies |
| Principle Hardware Design Engineer | Esperanto Technologies |
| Sr. Global Supply Chain Manager | Ex - Intel |
| Technical Leader - HW Development | EXFO |
| Hardware Architect | EXFO |
| BU Director | EXFO |
| Principal Engineer | Exo Inc. |
| Director | Exquisite Design / NASA |
| Signal Integrity Engineer | Extreme Engineering Solutions |
| Signal Integrity Engineer | Extreme Engineering Solutions |
| Electrical Engineer | Extreme Engineering Solutions |

| Job Title | Company |
|--|----------------------------------|
| Head of Partnerships | Flux.ai |
| Sr. Design Engineer | FNL Services |
| Lead Technologist | ForceFull Technology |
| Sr. Director of Engineering, Design | FormFactor Inc. |
| HW Engineer | Fortinet |
| Hardware Engineer | Fortinet |
| EE Manager | Fortinet |
| HW Engineer | Fortinet |
| Director of Hardware System Development | Fortinet |
| Signal Integrity Engineer | General Dynamics Mission Systems |
| Hardware Design | General Radar Corp |
| Engineering Manager | Geotech |
| Principal software Engineer | Gigamon |
| Sr. Staff Engineer | Gigamon |
| CEO | Gigaparsec |
| VP Engineering | GigaTest Labs |
| Hardware Engineer | Google |
| Senior Signal Integrity and Power Integrity Engineer | Google |
| Chip Package Design Architect | Google |
| Silicon Test Engineer | Google |
| Power Integrity Engineer | Google |
| Component Engineer | Google |
| Electrical Engineer | Google |
| Hardware Engineering Lead, ChromeOS | Google |
| Sr. FAE | Gowin Semiconductor Corp. |
| Sr. Manager, Field Applications Engineering | Gowin Semiconductors |
| Staff Engineer | Granite River Labs |
| VP of Global Engineering | Granite River Labs |
| Principle Engineer | Greensource Fabrication |
| Engineering Manager | Greensource Fabrication |
| Principal HW System Engineer | Groq, Inc. |
| Design Engineer | GSI Technology |
| VP Engineering | GSI Technology |
| Design and Solutions Specialist | Hellind Electronics |
| Master Technologist | Hewlett Packard Enterprise |
| Mechanical Engineer | Hewlett Packard Enterprise |
| Electrical/Hardware Engineering - Specialist | Hewlett Packard Enterprises |
| SAE | Hioki USA |
| Principal Engineer Photonics SI | Hirose |
| Design Engineer | Hisense Broadband Inc |
| Technical Director | Hisuntest |
| Director | Hitachi |
| Sr. Program Manager | Hitech Metal Fab, Inc. |
| PRESIDENT | HONEST COMPONENTS INC |
| Electrical Design Engineer | Honeywell Aerospace Technologies |
| Electrical Engineer II | Honeywell |
| M&A Advisor | Horizon M&A Advisor |
| Senior Staff Engineer | Huawei Canada |
| Power Integrity Expert | HUAWEI Italia |

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| Job Title | Company |
|---|-------------------------------------|
| Senior SI Engineer | IBIS O.F. & The MathWorks, Inc. |
| Signal and Power Integrity Engineer | IBM |
| Principal Research Staff | IBM |
| Research Scientist | IBM |
| Chair SCV | IEEE |
| Engineer | IEEE Santa Clara Valley APS Chapter |
| Vice Chair | IEEE-TEMS-SCV |
| Head of AI | Imagry |
| ASIC Solution Architect | IMEC IC-link |
| Service Manager | IMS Nanofabrication |
| Hardware Engineer | IMSAR LLC |
| Electrical Engineer | Inficon |
| CEO | InFind |
| Vice President, Wireless IoT Design Engineering | Infineon |
| Technical Project Manager | Infineon |
| Sr Apps Engr | Infineon |
| Principal Product Manager | Infinera |
| Hardware System Architect | Infinera |
| Data Center Cable, Connector Engineer | Intel |
| Signal Power Integrity Engineer | Intel |
| Field Applications Engineer | Intel |
| Principal Engineer | Intel |
| Director | Intel |
| Senior EDA Tools Software Engineer | Intel |
| PDE | Intel |
| Director | Intel |
| Product Engineer | Intel |
| Senior Staff Engineer | Intel |
| FPGA Silicon Design Engineer | Intel |
| Mechanical Engineer | Juniper Networks |
| Senior Director of Hardware Engineering | Juniper Networks |
| Signal Integrity Engineer | Juniper Networks |
| Sr. Staff Hardware Engineer | Juniper Networks |
| Digital Chip Architecture | Kandou Bus |
| CTO | Keeran.ai |
| Executive Director | KEI Systems |
| CEO | KEITA Smart Future Creations Inc |
| Distinguished Engineer | Keysight Technologies |
| Power Integrity Product Owner | Keysight Technologies |
| Strategic Planner & Datacom Technology Lead | Keysight Technologies |
| Senior Solutions Engineer | Keysight Technologies |
| Head of AI | Keysight Technologies |
| AI Program Manager | Keysight Technologies |
| Vice President & General Manager | Keysight Technologies |
| IC Design Manager | Kinetic Technologies |
| Artist | Kingman Ink |
| Director | Kinwong Electronic |
| CEO | Kipo AI |
| Hardware FPGA Engineer | KLA |
| Development Engineer | KLA-Tencor |

| Job Title | Company |
|---|----------------------------------|
| Design Studio Manager | Kohler |
| Industrial Design | Kohler |
| Senior Electrical Engineer | Kohler |
| Electrical Engineer | Kranze Technology Solutions |
| Lead Electrical Engineer | Kranze Technology Solutions |
| Applications Development Engineer | Kuraray America |
| Layout Engineer 2 | Kymeta |
| SI Engineer | Lattice Semiconductor |
| Design Validation Engineer | Lattice Semiconductor |
| Product Validation Engineer | Lattice Semiconductor |
| Distinguished Architect | Lattice Semiconductor |
| Electrical Engineer | Lawrence Livermore National Lab |
| Sr Project Engineer | LEMO USA |
| Principal Engineer, Electrical | Lumentum |
| Firmware Engineer | Lumentum |
| Principal Product Application Engineer | Luxshare-Tech |
| Embedded Software Consultant | MAB Labs Embedded Solutions |
| Consulting Engineer | MACOM |
| Director of Engineering | Marshall Radio Telemetry |
| Digital Engineer | Marvell |
| Test Engineer | Marvell |
| Validation Engineer | Marvell |
| Senior Vice President | Marvell |
| Senior Field Applications Engineer | Marvell |
| Director, Systems Application Engineering | Marvell |
| SVP Engineering Connectivity BU | Marvell |
| Semiconductor Engineer | Mask Operations |
| Manager, RF/PCB Toolbox | MathWorks |
| Senior Application Engineer | MathWorks |
| Manager, EM & RF Products | MathWorks |
| VP Sales | MATRIX USA |
| Materials/Operations Manager | MATRIX USA |
| CDO-Silicon | MatX |
| Advanced production tech | Maxar Technologies |
| Associate Software Engineer | Maxar Technologies |
| System Architect | MaxLinear Inc. |
| Dir SI/PI | MaxLinear Inc. |
| Director | MaxLinear Inc. |
| Application Manager | MaxLinear Inc. |
| Principal Engineer | Maxlinear Inc |
| VP/GM High Speed Interconnect | MaxLinear Inc. |
| Associate Engineer | Mayo Clinic |
| Engineer | MB Solutions |
| Electrical Engineer | MB Solutions |
| Sales Team Leader | MBK |
| CEO | McDonald, Watson, and Associates |
| Research | MCSP |
| CEO | Mcurich Inc |
| Senior Technical Manager | MediaTek |
| Staff Engineer | MediaTek |
| President | Mein Executive Group |

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| Job Title | Company |
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| Engineer | Melody Semiconductor LLC |
| MGR | MEMSPI |
| Director Apps. Eng. N.A. | Menta eFPGA |
| Electrical Engineer | Mercury Systems |
| Senior Principal Engineer | Mercury Systems |
| Sales | Meridian Technical Sales |
| Sr. Engineer | MERITRONICS |
| Network Hardware Engineer | Meta |
| SIPI Engineer | Meta |
| Hardware Engineer | Meta |
| Power Engineer | Meta |
| Signal and Power Integrity Engineer | Meta |
| RF Desense Engineer | Meta |
| SI Manager | Meta |
| Sr. PCB/FPC Design Engineer | Meta |
| Electrical Engineer | Meta |
| Product Design Engineer | Meta |
| SIPI EMC Manager | Meta |
| Engineer | Metawafer Technologies, Inc. |
| President | Metlink Associates |
| VP Worldwide Sales & Marketing | MFS Technology |
| Technical Sales | MFS Technology |
| Operation | Micas |
| CEO | MicroBin, Inc. |
| Senior Technical Staff Member | Microchip |
| Test Software Engineer | Microchip |
| Senior Technical Staff Engineer | Microchip Technology |
| Validation Engineer | Microchip Technology |
| FAE | MicroElecs Technical |
| CEO | Microglass |
| Principal Customer Systems Engineer | Micron |
| Principal Signal Integrity Engineer | Micron |
| Principal Test Development Engineer | Micron |
| Test Development Engineer | Micron |
| Director of ASIC Post Silicon | Micron |
| ASIC Characterization Engineer | Micron |
| Principle HW Engineer, SSD | Micron |
| PCIE Engineer | Micron |
| Director, Foundry and 3rd party IP's | Micron |
| Principal Signal Integrity Engineer | Micron |
| Principal Signal Integrity Engineer | Micron |
| Senior Hardware Engineer | Microsoft |
| Electrical Engineer | Microsoft |
| Senior Design Engineer | Microsoft |
| Principal Engineer | Microsoft |
| Validation Engineer | Microsoft |
| Media Director | Microwave Journal/Signal Integrity Journal |
| Engineer | Midasoft |
| Director, MIPI Technology | MIPI Alliance |
| Principal Signal Integrity Engineer | Mircon Inc. |

| Job Title | Company |
|---|--------------------------------------|
| Electrical Engineer | Molecular Devices |
| Design Engineer | Molex |
| Director, Interconnect Technology | Molex |
| Engineer, Field Application | Molex |
| Principal Signal Integrity Engineer | Molex |
| Applications Engineer | Monolithic Power Systems |
| Sr. Engineering Director | Montage Technology, Inc. |
| Design Engineer | Moog, Inc. |
| Senior Design Engineer | Moog, Inc. |
| SVP | Moschip Technologies |
| Systems Engineer | Motherlode Technical Services, LLC |
| Principal Engineer | Motional AD |
| Sr. Manager, Strategic Sourcing | Motive Technologies, Inc. |
| Distinguished Engineer | Motivo.ai |
| CEO | Mount Healy Capital |
| Power Architect | Movellus Circuits, Inc |
| Director RF Technologies | MPI Corporation |
| CEO - Chief Executive Officer | MultiLane |
| Senior Director | Murata |
| Electrical Engineering Researcher | National Renewable Energy Laboratory |
| Senior Technical Officer | NATO Support and Procurement Agency |
| Senior Engineer | NavTech |
| Engineering Director | NavTech |
| CEO | Neoconix |
| Project Engineer | NEOTech |
| Hardware Engineer | NetApp |
| SI Engineer | NetApp |
| Principal MTS | NetApp |
| Firmware/Hardware Engineer | Netflix Inc |
| COO | NetLAN Technologies |
| Founder / Tech. Lead | Neuma LLC |
| Sr. ASIC Engineer | Neuron IP |
| Digital Design Lead | Neuron IP Inc. |
| Director Engineering and Fab Operations | NextFlex |
| FHE Design and Process Engineer | NextFlex |
| Solutions Sales Advisor | NextGig Systems. Inc. |
| CEO | Nextgin Technology |
| Engineer | Nexyn Corporation |
| Product Development | NI Connect LLC |
| Senior Executive Advisory Engineer | Nittoboseki Co.,Ltd. |
| Sr Business Manager | NMB Technologies Corp. |
| Director | Nobi |
| HW Engineer | Nokia |
| Mechanical Design Engineer | Nokia |
| Technical Program Manager | Nokia |
| Data Scientist | Northeastern University |
| Staff Engineer Digital | Northrop Grumman |
| Associate Pathways Electrical Engineer | Northrop Grumman |
| Database Architect | Northrop Grumman |
| Sr. Hardware Engineer | NovAtel Inc |

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| SOC Design Engineer | Novelty Logic |
| Manager, Field Application Engineering | NTT Electronics America, Inc. |
| Founding Member and Director of Signal Integrity | Nubis Communications |
| Founding Member & Director of Signal Integrity | Nubis Communications |
| Senior Director of Product Management | Nubis Communications |
| CEO | Nubis Communications |
| Founder CTO | Nubis Communications |
| HW Networking Team Leader | NVIDIA |
| Systems Architect | NVIDIA |
| Principle Test Engineer | NVIDIA |
| Senior Systems Product Development Engineer | NVIDIA |
| VP, Mixed Signal Design | NVIDIA |
| High-speed Technical Lead | NVIDIA |
| Technical Program Manager, Automotive | NVIDIA |
| Sr. Test Engineering Manager | NVIDIA |
| Test Engineering Manager | NVIDIA |
| DGX Validation Engineer | NVIDIA |
| Sr Automotive Hardware Architect | NVIDIA |
| Director of Design Automation Research | NVIDIA |
| System Design Engineer | NVIDIA |
| Product Architect | NVIDIA |
| Senior PCB Development Support Engineer | NVIDIA |
| Hardware Systems Engineer | NXP Semiconductors |
| Eng. Tech | NXP Semiconductors |
| Senior Scientist | O-Net Communications |
| Senior Scientist | O-Net Communications |
| Senior Hardware Engineer | O-net communications |
| Applications Engineer | Onsemiconductor |
| Consultant | Ophiroptics |
| Senior Optomechanical Engineer | Opsys-tech |
| Chief Architect | OptAloT |
| Principal Engineer | OptAloT |
| Managing Director | Optech Technology „Co „LTD . |
| Hardware Engineer | Optiver |
| VP, High Speed Transceivers | OPTIWORKS/BIZLINK |
| CTO | Optomind Inc. |
| Hardware Engineer | Oracle |
| Component Engineer / Power | Oracle |
| Sr. Principal Component Engineer | Oracle |
| Component Engineer | Oracle |
| Production Event Specialist | Organic Ent Ltd |
| Design Engineer | Pacific Scientific |
| Director of Manufacturing | Pactron |
| Data Engineer | Palo Alto DC |
| CEO | Palo Alto Networks |
| Hardware Engineer | Palo Alto Networks |
| Signal Integrity Engineer | Palo Alto Networks |

| Job Title | Company |
|------------------------------------|---------------------------------|
| SI Engineer | Palo Alto Networks |
| Managing Director | Palo Alto Networks |
| PCB Quality Manager | Palpilot International Corp. |
| Technology Development Manager | Panasonic |
| Group Manager | Panasonic |
| Director | Panda Fund Corporation |
| Director Marketing | Parade Technologies |
| Principle Engineer | Parade Technologies |
| Staff Engineer, Analog Design | Parade Technologies |
| Design Engineer | Parade Technologies |
| Field Applications Engineer | Paragon Technical |
| Managing Partner | Paragon Technical |
| Consultant | PCB Market Catalyst |
| CEO | Plaid Semiconductors |
| CTO | Plaid Semiconductors |
| Programmer | Platform Solutions |
| President | PMK America Corporation |
| SVP/GM | POET Technologies |
| Vice President of Applications | Point2 Technology |
| Mentor | PPM Associates |
| Engineer | Precision Analog Design |
| Wireless and Systems Engineer | Precision Neuroscience |
| General Manager | Pressmaster |
| CEO | Primesoc Technologies |
| Patent Attorney | Private Practice |
| Principal Architect | Prodigy Technovations Pvt Ltd |
| Design Engineer | Proficient Design |
| Systems Engineer | Proficient Design |
| CEO | Proficient Design |
| Packaging R&D Engineer | Programmable Solutions Group |
| Electrical Engineer | Prolitech |
| CAD Beast | Promaxo |
| CTO | Proqur |
| CEO | Proqur |
| Applications Engineer | PTSL |
| Electrical Engineer | PUFFCO |
| Sr Director of BD | PUFsecurity |
| Product Engineer | Pulse Instruments |
| Sr. Manager, Engineering | Qorvo, Inc. |
| SR Test Engineer | QST Solutions, Inc |
| Electrical Engineer | Qualcomm |
| Principal Engineer | Qualitas semiconductor co., ltd |
| VP | Quantic Eulex |
| SPE Field Applications Engineering | Rambus |
| Sales Account Manager | Rambus |
| Senior Technical Director | Rambus |
| Sr. Director | Rambus |
| Senior Principal Engineer | Rambus |
| Package Engineer | Rambus |
| IP Strategy Manager | Rambus |
| Technical Director | Rambus |
| Hardware Design Manager | Ranovus Technology |

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| Senior Engineering Master | Rapidus Corporation |
| Principal Applications Engineer | Real Intent |
| Regional Sales Director | Real Intent |
| Senior Manager | RealTek |
| Research Scientist | Rebellions Inc. |
| Senior Hardware Engineer | Recogni |
| Head of System Hardware | Recogni |
| Technical Director | Resonac America |
| Project Manager | Rice Lake Weighing Systems |
| Engineer | Richard Reel |
| FAE | Richardson RFPD |
| Member of Technical Stuff, SIPI | Rivos Inc |
| SIPI Engineer | Rivos Inc |
| Packaging, SIPI and Thermal Mechanical Manager | Rivos Inc |
| Sr. Member of Technical Staff | Rivos Inc |
| Audio Technician | RM Productions |
| CEO | Robotics |
| Principal | RobustPhysics |
| Research Fellow | Rogers Corporation |
| Technology Manager- USB/DP/HDMI | Rohde & Schwarz |
| Characterization Engineer | Rohde & Schwarz |
| Senior Development Engineer DSP | Rohde & Schwarz |
| System Architect | ROHM SEMICONDUCTOR |
| Sales Account Manager | Rohm Semiconductor USA, Inc. |
| Sr RF Hardware Engineer | Roku |
| Sr. Partner | Roland Berger LP |
| Engineering | Rosenberger |
| Engineering | Rosenberger |
| Packaging Engineer | RR Donnelley & Sons |
| CTO | Rugged Energy |
| Director of Sales | RX Global |
| Owner | S an K precision |
| Consultant | S&P LLC |
| CEO | S.E. Tech |
| SI Engineer | SambaNova Systems |
| Sr. Principal SI Engineer | SambaNova Systems |
| Director | Sambanova Systems |
| Senior Director | SambaNova Systems |
| Senior Principal Hardware Engineer | SambaNova Systems |
| Director, Engineering | SambaNova Systems |
| Systems Architect | SambaNova Systems |
| VP Hardware and Systems Engineer | Sambanova Systems |
| Sr. Technical Program Manager | Sambanova Systems |
| Technical Program Manager | SambaNova Systems |
| Rack Systems Architect | SambaNova Systems |
| Senior Staff Application Engineer | Samsung Semiconductor Inc |
| Sr Director Engineering SoC Design Engineer | Samsung Semiconductor Inc |
| Principal Engineer | Samsung Semiconductor Inc |
| Principal Engineer | Samsung Semiconductor Inc |
| Staff Thermal Engineer | Samsung Semiconductor Inc |

| Job Title | Company |
|---|--|
| Strategic Technologist | Samtec |
| Active Systems Architect | Samtec |
| Signal Integrity Engineer | Samtec |
| Sr Design Engineer | Samtec |
| Field Engineer | Samtec |
| Electrical Engineer | Sandia National Laboratories |
| Principal Computer Engineer | Sandia National Laboratories |
| Systems Engineer | Sandia National Laboratories |
| Electronics Engineer | Sandia National Laboratories |
| FAE Manager | SCC |
| Principal Engineer Backplanes | Schroff nVent |
| Managing Director | Schweizer Electronics America |
| Consultant | Scientific Services |
| Staff Engineer | Sciton |
| HW & FW Engineer | Seabery Soluciones |
| Senior Staff SI | SEAKR Engineering |
| Power Electronics Engineer | SEAKR Engineering |
| Hardware Design Engineer | Sealevel Systems Inc |
| Sr Hardware Design Engineer | Sealevel Systems Inc |
| Test Engineer | Sealevel Systems Inc |
| Simulation & Verification Engineer | Seco S.p.a |
| Senior Simulation & Verification Engineer | Seco S.p.a |
| President | SemiQual |
| Editor | Semiwiki |
| Vice President, Technology | SI2 |
| Vice President | Sicon International |
| Product Specialist | Siemens EDA |
| Support Application Engineer | Siemens EDA |
| Field Application Engineer | Siemens EDA |
| Sr. AEC | Siemens EDA |
| Principal Engineer | Siemens EDA |
| Director of Community | Sierra Circuits |
| PCB Design Engineer | Sierra Circuits |
| Systems Engineering | SiMa |
| Sr. Principal Engineer | SiMa.ai |
| Manager | Simply Smart Home |
| VP of Systems Engineering | Siomyx Inc. |
| Senior ASIC Design Engineer | Socionext America Inc. |
| SI/PI Engineer | Sony Semiconductor Solutions Corporation |
| Senior Analog IC Design Engineer | Spectra7 Microsystems |
| Design Engineer | Stanford Research Systems |
| Systems Engineer | Stanford University |
| Co-Founder | Stealth |
| Sr. Applications Engineer | STMicroelectronics |
| Hardware Engineer | STMicroelectronics |
| CEO | Strategic IP Initiatives, Inc. |
| Lab Director | Strategic IP Initiatives, Inc. |
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| Field Application Engineer | Sunway Communication |
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| Product Architect | Teledyne LeCroy |
| Signal and Power Integrity Engineer | Teradyne, Inc. |
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| Design Verification Engineer | Texas Instruments |

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| Design Manager | Texas Instruments |
| Analog Design and Systems Manager | Texas Instruments |
| Product Manager | Texas Instruments |
| Analog Design Engineer | Texas Instruments |
| Characterization Manager | Texas Instruments |
| Coordinator of Control Platform Solutions | TRUMPF Huettinger |
| Senior Electronic Engineer | TRUMPF Huettinger |
| Sr. Director Technology Development | TTM Technologies |
| VP Technology | TTM Technologies |
| Director of Commercial Technology | TTM Technologies |
| Site Applications Engineer | TTM Technologies |
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