DesignCon by informa

2027
February 2–4
Santa Clara Convention Center
Santa Clara, California



# 2026 ATTENDEE LIST AT A GLANCE

Partial list pulled from 2026 attendee registration data.

| COMPANY | TITLE |
|---|---|
| A&D Company, Americas | Industrial Product Manager |
| Abbott Diabetes Care, Incorporated | Senior Electrical Engineer |
| Accenture | Chief Research Scientist |
| Accenture | Technology Infrastructure Manager |
| Adage | Analyst |
| Adeia | Principal Engineer |
| Advanced Micro Devices Inc. | SMTS Systems Design Engineer |
| Advantest | Principal Strategist |
| Advantest | Application Engineer |
| Advantest | Semiconductor Process Engineering Lead |
| Advantest | Silicon Photonics Applications Engineer |
| AEHR Test Systems | Electrical Design Engineer |
| Aetherflux, INC. | Avionics Engineer |
| Allied Telesis Inc | HW DVT Engineer |
| Allied Telesis Inc. | DVT Engineer |
| Alpha & Omega Semiconductor | Principal Design Enginer |
| Alpha and Omega Semiconductor | Senior Staff Engineer |
| Alphawave | Senior Test Engineer |
| Altaflex | Program Manager |
| Altera | FPGA Design Engineer |
| Altera | Field Applications Engineer |
| Altera | Engineering Manager |
| AMAT | Hardware Engineer |
| AMAT | RF Engineer |
| Amazon | SIPI Engineer |
| Amazon | Software Engineer |
| Amazon | Mechanical Engineer |
| AMD | SIPI engineer |
| AMD | Design Engineer |
| Ampere Computing | Principle Engineer |
| Ampere Computing | Signal Integrity Engineer |
| Amphenol | Applications Development Engineer |
| Amphenol | Application Engineering Manager |
| Amphenol | Signal Integrity Engineer |

| COMPANY | TITLE |
|---|---|
| Analog Aficionados | National Semi Conductor |
| Analog Devices | Senior Principle Engineer |
| Analog Devices | Principal Member of the Technical Staff |
| Analog Devices | Senior Engineering Manager |
| Annapurna Labs - Amazon | Hardware Engineer - Interconnects |
| Ansys | VP Product Development |
| Apple | Hardware Engineer |
| Apple | Validation Engineer |
| Apple | Signal Integrity Engineer |
| Applied Brain Research | ASIC Development Lead |
| Applied Materials | ECAD Application Manager |
| Arista | Thermal Engineer |
| Arista | Mechanical Engineer |
| Array Labs | Electrical Engineer |
| Arrow | Field Applications Engineer |
| Artists in Innovation | Executive Advisor |
| Astera Labs | Electrical Quality Validation Engineer |
| Astera Labs | Systems Engineering Director |
| Astera labs | Packaging SI/PI Engineer |
| Astera Labs | Analog Design Engineer |
| Astera Labs | Senior Director - Product Applications |
| BizLink Technology, Inc | Senior Field Application Engineer |
| Broadcom | Package Design Engineer |
| Broadcom | Master Engineer |
| Cadence | Product Engineering Architect |
| Cadence | Mechanical Engineer |
| Cadence | Design Engineer |
| Calix, Inc. | Hardware Systems Quality Engineer |
| Celestica | Hardware Engineer |
| Cerebras Systems | Hardware Design Engineer |
| Cerebras Systems | Principal Engineer |
| Ciena | Hardware Engineer |
| Ciena | Senior Analog Architect |
| Ciena | Analog Engineer |

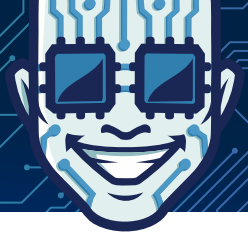

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| COMPANY | TITLE |
|---|---|
| Cisco | Hardware Engineer |
| Cisco | Mechanical Design Engineer |
| Credo Semiconductor | Principle Engineer |
| Credo Semiconductor | Application Engineering Manager |
| Credosemi | System Design Engineer Director |
| Crystal Group | High Speed Engineer |
| Crystal Group | Electrical Engineer |
| Dell Technologies | Senior Principal Power Engineer |
| Dell Technologies | System Architect |
| Deloitte | ServiceNow Application Architect |
| Delta Pacific Technologies | Senior Director Test Engineering |
| D-Matrix | HW Systems Technician |
| DMEA | Electronics Engineer |
| Dolby Laboratories | Staff Engineer |
| Doordash | Hardware Engineer |
| Dupont | Systems Architect |
| Dynamic Electronics | Engineering Director |
| Dynamic Electronics | VP, Engineering |
| Dynamic Test Solutions | Senior Design Manager |
| Dynamic Test Solutions | PCB Design Engineer |
| Dynamic Test Solutions | Engineering Project Manager - CID |
| Edveon Inc | Design Verification Engineer |
| Edveon Technologies | Senior Verification Engineer |
| Enclustra | Founder Chief Innovation Engineer |
| Epoch International Enterprises, Inc. | Software Support Engineer |
| Exfo | FPGA Technical Leader |
| Extreme Engineering Solutions | Signal Integrity Engineer |
| Fast Integrated Circuit Techniques LLC | Chief Scientist |
| FIT | Development Engineer |
| Flexcompute | Principal Solutions Architect |
| Flexcompute | Senior Scientist |
| Flexcompute | RF Applications Engineer |
| Fortinet Inc. | Hardware Architect |
| Foxconn Interconnect Technology | Applications Engineering Manager |
| Foxconn Interconnect Technology | Field Application Engineer |
| Fujikura America Inc | Application Engineering Manager |
| Fujikura America, Inc | Engineering Manager |
| GE Healthcare | Electrical Engineering - Signal & Power Integrity |
| Generac | Reliability Quality Lead |
| General Dynamics | Senior Principal Engineer |
| General Dynamics | EMI Design Engineer |
| Google | Hardware Engineer |

| COMPANY | TITLE |
|---|---|
| Google | Power Engineer |
| Google | Hardware Signal Integrity Test Engineer |
| Google | Chip Packaging Design Engineer |
| Granite River Labs | Senior Lead Engineer |
| Granite River Labs | Engineering Fellow and General Manager |
| HCL Technologies | Group Technical Manager |
| HCR Electronics | Senior Software Engineer |
| HPE | Senior Hardware Engineer |
| HPE | System Power Engineer |
| HPE | Principle Hardware Engineer |
| HPE | Thermal Engineer |
| IBM | Hybrid Data & AI Storage Specialist |
| IBM | Software Defined SME |
| IEEE | Researcher |
| IEEE | Educational Activities Chair |
| Infineon | EMC engineer |
| Infineon | Senior Marketing Manager |
| Intel | Electrical Engineer |
| Intuitive Surgical | Staff Research Engineer |
| Intuitive Surgical | Engineering Tech |
| Intuitive Surgical | Electrical Engineer |
| Japan Aviation Electronics Industry, Ltd. | Engineering Manager |
| Juniper Networks | Senior Director, Hardware Engineering |
| Kaga FEI America | Field Application Engineer |
| Kaiser | Medical Director |
| Kaiser | Physician |
| Keysight | Digital Solution Architect |
| keysight | Electrical Engineer |
| Kirkwood Radio Laboratories | Chief Scientist |
| KLA | PCBA Designer |
| KLA | Electrical Engineer |
| Lam Research | Firmware Engineer |
| Lightmatter | Packaging Technologist |
| Lightmatter | Power Delivery Engineer |
| Lightmatter | Signal Integrity Engineer |
| Lithos Energy | VP, Electrical Engineering |
| Lithos Energy | Staff Electrical Engineer |
| Lithos Energy | Senior Electrical Engineer |
| Lithos Energy | Director of Battery Engineering |
| Logitech | Electrical Engineer |
| Lucid Motors | Wire Harness Electrical Schematics Engineer |
| Lucid Motors | Senior Staff Engineer - ADAS SOFTWARE |

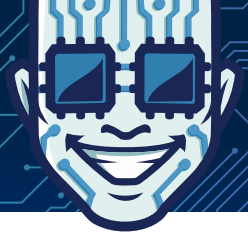
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| COMPANY | TITLE |
|---|---|
| MACOM | Consulting Engineer |
| MACOM | Principal Application Engineer |
| Marvell | Senior Principal Applications Engineer - Product Marketing |
| Marvell | Principal Validation Engineer |
| Marvell | Director Design Verification |
| MathWorks | Application Engineer |
| MatX | Hardware System Lead |
| MatX | Advanced Packaging Design Lead |
| Mercury Systems Inc | Expert Electrical Engineer |
| Meta | ASIC Package Engineer |
| Meta | Signal Integrity Engineer |
| Meta | Network Engineer (Foundation & Support) |
| Meta | Advanced Packaging Quality and Reliability Eng |
| Microchip | Test Software Engineer |
| Micron | Principal Design Engineer |
| Micron | Test Development Engineer |
| Microsoft | Hardware Engineer |
| Microsoft | Quantum Electrical Engineer |
| Microsoft | Principal Thermal Technologist |
| Molex | Innovation Engineer |
| Molex | Senior Software Engineer / Developer |
| Molex | Hardware Engineer |
| Monolithic Power Systems | Field Applications Engineer |
| Moog, Inc | Senior Software Engineer |
| NATO Support and Procurement Agency | Senior Technical Officer |
| NavTech | Engineering Director |
| NetApp | Hardware Engineer |
| Nexthop | Thermal Hardware Engineer |
| Nokia | Technical Manager |
| Nokia | Design Engineer |
| Nokia | Mechanical Design Engineer |
| Northrop Grumman | Staff Engineer |
| Northrop Grumman | Associate RF Microwave Deign Engineer |
| nVent / Schroff | Principal Engineer Backplanes & Signal Integrity |
| Nvidia | Senior Hardware Engineer |
| Nvidia | Signal Integrity Engineer |
| Nvidia | Photonics Engineer |
| Nvidia | Senior Optical Mixed Signal Design Validation Engineer |
| OLIX | Electro-Optics Engineer |
| Open AI | Technical Engineer |

| COMPANY | TITLE |
|---|---|
| OpenLight Photonics | Design Methodology & Enablement Engineer |
| Oracle | Supply Chain Engineer |
| PacketMicro | Software Engineer |
| Palo Alto Networks | Principal Engineer Hardware |
| Palo Alto Networks Inc. | Senior Principal Engineer |
| Palo Alto Ventures | Managing Director |
| Palpilot InternationaL | Quality Manager |
| Panasonic | Strategic Development Manager |
| Philips | Staff Systems Engineer |
| PICadvanced | Transceiver and Systems Coordinator |
| PICadvanced | Hardware Engineer |
| Pivotal Aero | Electrical Engineer |
| Pivotal Aero | Electromechanical Engineer |
| Pivotal Systems Corporation | Principal System Engineer |
| PJi | Mechanical Engineer |
| Planet Labs | Electrical Engineer |
| Point2 Technologies | Signal Integrity Engineer |
| Point2 Technology | IC Design Engineer |
| Product Market Pro | PMF Architect |
| Qnity Electronics | R&D Senior Technical Associate |
| Qnity Electronics | RF Application Engineer |
| Qorvo | Principal Systems Engineer |
| Qorvo | Senior MEMS Test Engineer |
| Qualcomm | Principal Engineer |
| Qualcomm | Tech Specialist |
| Quest America Inc | Director - Engineering |
| Quilter | Staff Electrical Engineer |
| Rambus | Senior Director |
| Rambus | Application Engineer |
| Reed Semiconductor | Field Application Engineer |
| Reflex Robotics | Electrical Engineer |
| RefleXion | Physicist |
| RF Associates North Inc. | Engineer |
| Rivet Industries | Electrical Engineer |
| Rivian | Staff PC Board Designer |
| Robert Bosch GmbH | Senior Engineer |
| Robust AI | Principal Electrical Engineer |
| Roche Sequencing | Principle IC Engineer |
| Rohde & Schwarz | Marketing Engineer |
| Rohde & Schwarz | Vice President |
| Roku Inc | Senior Hardware Engineer |
| Rosenberger | Product Manager |
| Rosenberger | R&D RF Design Engineer |

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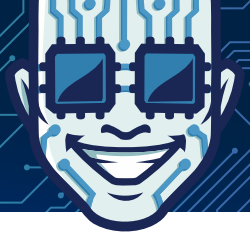
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| SambaNova Systems | Senior Principal Engineer |
| SambaNova Systems | Systems Architect |
| Samsung | Senior Staff Engineer |
| Samsung | Principal Engineer |
| Samtec | Signal Integrity Engineer |
| Samtec | Field Applications Engineer |
| Samtec | Electrical Engineer |
| Samtec | Optics Application Engineer |
| Sandia National Laboratories | Computer Engineer |
| Sandia National Laboratories | Principle Electrical Engineer |
| Sandisk | Signal Integrity Engineer |
| Sandisk | Signal and Power Integrity Technologist |
| Sandisk | Staff Engineer |
| Schaeffler Group USA | Head of Advanced Innovation - West Coast |
| Schlenk Metallfolien GmbH & Co. KG | Head of Technical Service |
| Schweizer Electronic (Jiangsu) Co. Ltd. | Director of Advanced Integration and Innovation |
| Seagate Technology | Electrical Design and Signal Integrity Engineer |
| SEAKR Engineering | Principal System Architect |
| SEAKR Engineering | Senior Staff Engineer, Signal and Power Integrity |
| Seyond | Director of Electrical Engineering |
| SFSU | Emeritus Professor of EECS at San Francisco State University |
| Siemens EDA | Product Engineer |
| Siemens EDA | R&D Engineer |
| Siemens EDA | Technology Manager |
| Silicon Creations | Director of Hardware Engineering |
| Silicon Creations | Hardware Validation Engineer |
| Simplehuman | VP Of Technology |
| Simplexity Product Development | Principal Engineer |
| Skydio | Design Engineer |
| Slac | Staff Engineer |
| Smiths Interconnect | Electrical Design Engineer |
| Smiths Interconnect | Product Line Manager |
| Solidigm | Hardware Engineer |
| Source Photonics | Technical Director |
| Space Startup | Principal Design Engineer |

| COMPANY | TITLE |
|---|---|
| SpaceBilt | Expert Advisor |
| SpaceX | Manager, Satellite Engineering |
| SpaceX | Space Laser Engineer II |
| ST Microelectronics | Field Applications Engineer |
| Stanford University | Data Manager |
| Stanford University | Systems Engineer |
| Stryker | Senior Staff Manufacturing Engineer |
| Stryker | Principal Engineer |
| Supermicro | Hardware Designer |
| Synopsys | Distinguished Architect |
| Synopsys | Hardware Engineering Manager |
| Synopsys | Technical Product Manager |
| Synopsys | Research and Development Software Engineer |
| TE Connectivity | Principal Engineer |
| TE Connectivity | Product Development Engineer |
| TE Connectivity | Staff Electrical Systems Engineer |
| Teledyne LeCroy | Software Development Engineer |
| Teledyne LeCroy | Product Manager |
| Tensordyne | Hardware Engineer |
| Teradyne | Principle Engineer |
| Teradyne | Signal and Power Integrity Engineer |
| Teradyne | Systems Engineer |
| Tesla | Staff Hardware Engineer |
| Tesla | IT Infrastructure Engineer |
| Texas Instruments | Advanced Power Design Solutions Manager |
| Texas Instruments | Hardware Engineer |
| Texas Instruments | Validation Engineer |
| Texas Instruments | Applications Engineer |
| Trinity Technologies | Field Engineer |
| Viavi Solutions | Hardware Engineer |
| Viavi Solutions | Senior Principal - HW Engineering |
| Vicor | Application Engineer |
| Walmart | Senior Software Engineer |
| Weather Stream Inc. | Radiometer Systems Engineer |
| zoox | Mechanical Test engineer |
| zoox | Hardware Test Engineer |
| ZPE Systems | Hardware Lead |

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*Source: 2026 Registration Report*