DesignCon 2016 Program to Offer Unique Electronic Design Educational Opportunities via Free Workshops, Meetups & Chiphead Theater

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SAN FRANCISCO, Dec. 16, 2015 /PRNewswire/ -- DesignCon, the premier conference for chip, board and systems design engineers in the high-speed communications and semiconductor communities, today announced key offerings for its 2016 event. This year's program will offer free workshops with the Keysight Education Forum, speed training in the Chiphead Theater and meetups covering a wide range of in-depth topics.

DesignCon will take place January 19-21, 2016 at the Santa Clara Convention Center in Santa Clara, CA. **For more information and to take advantage of Early Bird registration pricing, visit: designcon.com.**

Keysight Education Forum

Attendees will have the opportunity to gather new methods and techniques from Keysight Technologies experts during free education workshops. The Forum will run Wednesday-Thursday during the event and cover a wide range of industry topics including measurement, signal integrity, testing and more.

Wednesday, January 20

- *Mastering Measurement Requirements for 16GBIT/S Devices with PCI Express 4.0*
- *DDR4 Bus Level Signal Integrity Insight*
- *Identify and Remove Crosstalk from your High-Speed Signals*
- *USB-PD (Power Delivery) Testing Over Type-C*

Thursday, January 21

- *Overcoming Power Integrity Challenges in Design, Characterization and Debug of Digital Electronics*
- *Updates on Measurement Solutions for PAM-4 Signaling*
- *Signal Integrity Tips and Techniques Using TDR, VNA and Modeling*
- *Testing Displayport Over the USB Type-C Connector*

For more information on all of Keysight Technologies' educational offerings and to register, visit: designcon.com/santaclara/expo/keysight-education-forum.php

Chiphead Theater

Located on the expo floor, attendees can join industry leaders in the Chiphead Theater for speed training, tear downs, educational opportunities, market analysis, and some laughs. Programming will run Wednesday-Thursday and feature eight unique presentations by speakers from Teledyne LeCroy, Qualcomm, Picotest, and more.

Meetups

Located in a specially designed Meetup room (M1), participants will have the opportunity to gather with peers and colleagues to explore topics related to signal integrity or power integrity. These informal sessions are designed to offer a comfortable place to meet and share ideas. The program will feature the following specific meetups:

- Power Integrity
- Analog Aficionados
- SI-List Meetup
- DesignCon Authors Meet and Greet
- IEEEP370 Standard

"Our objective each year is to bring relevant and useful educational opportunities to all of our attendees, providing unique learning opportunities beyond the in-depth conference program," said Janine Love, DesignCon Technical Program Director. "Offerings such as the Chiphead Theater and Keysight Technologies' workshops are great opportunities for both EXPO and conference attendees who are looking to gain additional insight on content covered in the conference sessions."

<u>2016 Conference Program</u>

The DesignCon Technical Conference Program consists of 14 tracks covering all aspects of electronic design, from chips to boards and systems. Through more than 100 technical paper sessions, panels, presentations, and tutorials, conference attendees will gather the latest theories and practical solutions to the challenging problems shared across the industry in design and verification. Specifically, critical topics covered include signal integrity, power integrity, jitter, crosstalk, test and measurement, parallel and memory interface design, ICs, semiconductor components and more. **To see the full lineup and start building your onsite schedule, visit: designcon.com/santaclara/scheduler/list.**

Register for a Media Pass: designcon.com/santaclara/media-center/

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About DesignCon

DesignCon is the world's premier conference for chip, board and systems design engineers in the high-speed communications and semiconductor communities. DesignCon, created by engineers for engineers, takes place annually in Silicon Valley and remains the largest gathering of chip, board and systems designers in the country. This four-day technical conference and expo combines technical paper sessions, tutorials, industry panels, product demos and exhibits from the industry's leading experts and solutions providers. More information is available at: designcon.com/santaclara. DesignCon is organized by UBM Americas, a part of UBM plc (UBM.L), an Events First marketing and communications services business. For more information, visit ubmamericas.com.

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